



Attorney Docket No.: YOR920030603US1
Customer Number 48062

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Luk et al.
Docket No.: YOR920030603US1
Serial No.: 10/751,714
Filed: January 5, 2004
Group: 2826
Examiner: J.P. Mondt

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: Susan Justina Date: December 13, 2005

Title: Amplifier Using Gated Diodes

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicants submit herewith twenty (20) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

Kevin M. Mason

Date: December 13, 2005

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IPW

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TRANSMITTAL LETTER

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

1. Amendment and Response to Office Action;
2. Substitute Formal Drawings 2B, 5B, 7 and 9;
3. Marked up Figures 2B, 5B, 7 and 9;
4. Transmittal of Formal Drawings; and
5. Formal Drawings- 20 sheets

There is no additional claim fee due in connection with the Response.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

[Signature]
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Dated: December 13, 2005